

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Paul D. Kartschoke et al.

Serial No.: 10/605,683

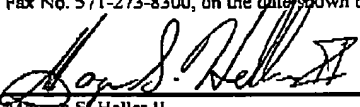
Filed: October 17, 2003

Title: Method Of Reducing Instantaneous Current
Draw And An Integrated Circuit Made Thereby

Group Art Unit: 2825

Examiner: Suchin Parihar

Attorney Docket No.: BUR920030114US1
(02016-00167)

<input checked="" type="checkbox"/> CERTIFICATION OF FACSIMILE TRANSMISSION	
I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office, Fax No. 571-273-8300, on the dates shown below.	
 Morgan S. Heller II	3/06/06 Date

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 6, 2006

RESPONSE TO FIRST OFFICE ACTION

This is in response to the Office Action (paper no. 20051103) mailed from the United States Patent and Trademark Office on December 5, 2005, with respect to the above-identified application. A Status of the Claims, an Amendment to the Specification and Remarks concerning the Office Action start, respectively, on pages 2, 6 and 7.